## **SIEMENS**

## **Data sheet**

6ES7216-2BD23-0XB0

 $^{***}$  Spare part  $^{***}$  SIMATIC S7-200, CPU 226 Compact unit, AC power supply 24 DI DC/16 DO relay 16/24 KB progr./10 KB data, 2 PPI/user-programmable interface



Figure similar

Supply voltage	
Rated value (AC)	
• 120 V AC	Yes
• 230 V AC	Yes
Load voltage L+	
<ul> <li>Rated value (DC)</li> </ul>	24 V
<ul> <li>permissible range, lower limit (DC)</li> </ul>	5 V
<ul> <li>permissible range, upper limit (DC)</li> </ul>	30 V
Load voltage L1	
<ul> <li>Rated value (AC)</li> </ul>	100 V; 100 V AC to 230 V AC
<ul> <li>permissible range, lower limit (AC)</li> </ul>	5 V
<ul> <li>permissible range, upper limit (AC)</li> </ul>	250 V
<ul> <li>permissible frequency range, lower limit</li> </ul>	47 Hz
<ul> <li>permissible frequency range, upper limit</li> </ul>	63 Hz
Input current	
Inrush current, max.	20 A; at 264 V
from supply voltage L1, max.	320 mA; 40 to 160 mA (240 V); 80 to 320 mA (120 V); output current for expansion modules (5 V DC) 1 000 mA
Encoder supply	
24 V encoder supply	
• 24 V	Yes; Permissible range: 20.4V to 28.8V
Short-circuit protection	Yes; electronic at 400 mA
<ul> <li>Output current, max.</li> </ul>	400 mA
Power loss	
Power loss, typ.	17 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files
Work memory	
<ul><li>integrated (for program)</li></ul>	24 kbyte; 16 KB with active run-time edit
<ul><li>integrated (for data)</li></ul>	10 kbyte
Backup	
• present	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering
Battery	
Backup battery	
<ul> <li>Backup time, max.</li> </ul>	100 h; (min. 70 h at 40 °C); 200 days (typ.) with optional battery module
CPU processing times	

for bit operations, max.	0.22 μs
Counters, timers and their retentivity	
S7 counter	
• Number	256
Retentivity	200
— adjustable	Yes; via high-performance capacitor or battery
— lower limit	1
— upper limit	256
Counting range	
— lower limit	0
— upper limit	32 767
S7 times	000
• Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
— upper limit	64
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to
Data awas and their naturalisits.	54 min
Data areas and their retentivity	
Flag	
• Size, max.	32 byte
Retentivity available	Yes; M 0.0 to M 31.7
<ul> <li>of which retentive with battery</li> </ul>	0 to 255, via high-performance capacitor or battery, adjustable
of which retentive without battery	0 to 112 in EEPROM, adjustable
Hardware configuration	
Number of expansion units, max.	7; Only expansion modules of the S7-22x series can be used. Due to the
	limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
Analog inputs/outputs, max.	35; max. 28 inputs and 7 outputs (EM) or max. 0 inputs and 14 outputs (EM)
<ul><li>Analog inputs/outputs, max.</li><li>Digital inputs/outputs, max.</li></ul>	148; max. 128 inputs and 120 outputs (CPU+EM)
<ul><li>Analog inputs/outputs, max.</li><li>Digital inputs/outputs, max.</li><li>AS-Interface inputs/outputs, max.</li></ul>	
<ul> <li>Analog inputs/outputs, max.</li> <li>Digital inputs/outputs, max.</li> <li>AS-Interface inputs/outputs, max.</li> </ul> Digital inputs	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)
Analog inputs/outputs, max.     Digital inputs/outputs, max.     AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24
Analog inputs/outputs, max.     Digital inputs/outputs, max.     AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)
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Analog inputs/outputs, max.     Digital inputs/outputs, max.     AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24  Yes; optionally, per group
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage Rated value (DC)	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage Rated value (DC) for signal "0"	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage Rated value (DC) for signal "0" for signal "1"	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage Rated value (DC) for signal "0" for signal "1"  Input current	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input Input voltage Rated value (DC) for signal "0" for signal "1" Input current for signal "1", typ.	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V
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Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Number of digital inputs  Number of digital inputs  Source/sink input  Input voltage Rated value (DC) for signal "0" for signal "1"  Input current for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  — parameterizable	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Number of digital inputs  Source/sink input Input voltage Rated value (DC) for signal "0" for signal "1" Input current for signal "1", typ. Input delay (for rated value of input voltage) for standard inputs  — parameterizable — at "0" to "1", min.	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms
Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  Number of digital inputs  Source/sink input Input voltage Rated value (DC) for signal "0" for signal "1" Input current for signal "1", typ. Input delay (for rated value of input voltage)  for standard inputs  parameterizable at "0" to "1", min. at "0" to "1", max.	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms
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Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage  Rated value (DC) for signal "0" for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable  at "0" to "1", min.  at "0" to "1", max.  for interrupt inputs  parameterizable  for technological functions  parameterizable  Cable length  shielded, max.  unshielded, max.	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz
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Analog inputs/outputs, max. Digital inputs/outputs, max. AS-Interface inputs/outputs, max.  AS-Interface inputs/outputs, max.  Digital inputs  Number of digital inputs  Source/sink input  Input voltage  Rated value (DC) for signal "0" for signal "1"  Input current  for signal "1", typ.  Input delay (for rated value of input voltage)  for standard inputs  parameterizable at "0" to "1", min. at "0" to "1", max.  for interrupt inputs  parameterizable for technological functions  parameterizable  Cable length shielded, max. unshielded, max.  Digital outputs  Number of digital outputs  Short-circuit protection  Switching capacity of the outputs	148; max. 128 inputs and 120 outputs (CPU+EM) 62; AS-Interface A/B slaves (CP 243-2)  24 Yes; optionally, per group  24 V 0 to 5 V min. 15 V  2.5 mA  Yes; all 0.2 ms 12.8 ms  Yes; I 0.0 to I 0.3  Yes; (E 0.0 to E 1.5) 30 kHz  500 m; Standard input: 500 m, high-speed counters: 50 m 300 m; not for high-speed signals  16; Relays No; to be provided externally
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Output voltage	
for signal "1", min.	L+/L1
• for signal 1 , min.  Output current	Life
• for signal "1" rated value	2 A
• for signal "0" residual current, max.	0 mA
Output delay with resistive load	UIIIA
• "0" to "1", max.	10 ms; all outputs
• "1" to "0", max.	10 ms; all outputs
Parallel switching of two outputs	To ms, all outputs
• for uprating	No
Switching frequency	INC
of the pulse outputs, with resistive load, max.	1 kHz
Total current of the outputs (per group)	I NIZ
all mounting positions	
— up to 40 °C, max.	10 A
horizontal installation	IU A
— up to 55 °C, max.	10 A
Relay outputs	10 A
Number of relay outputs	16
<ul> <li>Number of relay outputs</li> <li>Number of operating cycles, max.</li> </ul>	10 000 000; mechanically 10 million, at rated load voltage 100 000
	10 000 000, moonameany to million, at fateu load voltage 100 000
Cable length  • shielded, max.	500 m
unshielded, max.	150 m
unsheded, max.  Analog inputs	100 111
	Or Amelow notantiamentary resolution 0 hit
Number of analog potentiometers	2; Analog potentiometer; resolution 8 bit
Encoder	
Connectable encoders	V
• 2-wire sensor	Yes
— permissible quiescent current (2-wire sensor), max.	1 mA
1. Interface	
Interface type	Integrated RS 485 interface
Protocols  ● MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
• PPI	Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s
serial data exchange	Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
MPI	
Transmission rate, min.	19.2 kbit/s
Transmission rate, max.	187.5 kbit/s
2. Interface	
Interface type	Integrated RS 485 interface
Protocols	
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
• PPI	Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s
serial data exchange	Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
Integrated Functions	
Counter	
Number of counters	6; High-speed counters (30 kHz each), 32 bit (incl. sign), can be used as up/down counters or for connecting 2 incremental encoders with 2 pulse trains offset by 90° (max. 20 kHz (A/B counters)); parameterizable enable and reset input; interrupt facilities (incl. call of subroutine with any content) when the

	setpoint is reached; reversal in counting direction, etc.
Counting frequency, max.	30 kHz
Number of alarm inputs	4; 4 rising edges and/or 4 falling edges
Potential separation	
Potential separation digital inputs	
<ul> <li>between the channels</li> </ul>	Yes; Optocoupler
<ul> <li>between the channels, in groups of</li> </ul>	13 and 11
Potential separation digital outputs	
<ul> <li>between the channels</li> </ul>	Yes; Relays
<ul> <li>between the channels, in groups of</li> </ul>	4, 5 and 7
Permissible potential difference	
between different circuits	500 V DC between 24 V DC and 5 V DC; 1500 V AC between 24 V DC and 230 V AC
Degree and class of protection	
IP degree of protection	IP20
Ambient conditions	
Ambient temperature during operation	
<ul> <li>horizontal installation, min.</li> </ul>	0 °C
<ul> <li>horizontal installation, max.</li> </ul>	55 °C
<ul> <li>vertical installation, min.</li> </ul>	0 °C
<ul> <li>vertical installation, max.</li> </ul>	45 °C
Air pressure acc. to IEC 60068-2-13	
<ul> <li>permissible range, lower limit</li> </ul>	860 hPa
<ul> <li>permissible range, upper limit</li> </ul>	1 080 hPa
Relative humidity	
Operation, min.	5 %
<ul> <li>Operation, max.</li> </ul>	95 %; RH class 2 in accordance with IEC 1131-2
configuration / header	
configuration / programming / header	
Command set	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
<ul> <li>Program processing</li> </ul>	free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
<ul> <li>Program organization</li> </ul>	1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
<ul> <li>Number of subroutines, max.</li> </ul>	64
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
Know-how protection	
<ul> <li>User program protection/password protection</li> </ul>	Yes; 3-stage password protection
connection method / header	
Plug-in I/O terminals	Yes
Dimensions	
Width	196 mm
Height	80 mm
Depth	62 mm
Weights	
Weight, approx.	660 g

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